

	5024 (low)	5495 / 5496 / 5497 (mid)	5068 / 5153 (high)	5918
Industry Standard	IEEE 1386.1 (PMC)	IEEE 1386.1 (PMC)	IEEE 1386.1 (PMC) VITA 32 (PrPMC)	IEEE 1386.1 (PMC) VITA 20 (ccPMC)
Application	Low cost, slave PCI, Air Cooled. FP and Rear I/O	Low to High cost, Air cooled, FP I/O	Intermediate cost, master/slave PCI, Air cooled, FP I/O, uCLinux, DRAM, Flash, PrPMC	Low to High cost, master/slave PCI, Conduction cooled, Rear I/O
FPGALeS Density	20K	16K / 40K / 120K	35K / 70K	16K / 40K / 120K
FPGA Family	Cyclone II	Cyclone III	Cyclone II	Cyclone III
FPGA in Standard Product	EP2C20Q240C8N	EP3C16F484I7N (5495) EP3C40F484I7N (5496) EP3C120F484I7N (5497)	EP2C35F672I8N (5068) EP2C70F672I8N (5153)	EP3C16F484I7N (5918) EP3C40F484I7N (5919) EP3C120F484I7N (5920)
FPGA 4Kbit (M4K) Memories	52	N/A	105 / 250	N/A
FPGA 8Kbit (M9K) Memories	N/A	56 / 126 / 432	N/A	56 / 126 / 432
FPGA 18x18 Multipliers	26	56 / 126 / 288	35 / 150	56 / 126 / 288
FPGA PLL Support	No	Yes – Qty 4	Yes – Qty 4	Yes – Qty 4
ECM Modules Supported	0 to 4	0 to 4	0 to 4	0 to 4
FPGA signals per ECM	28	28	28	28
I/O signals per ECM	16	16	16	16
Power Rail	5V	3.3V	3.3V	3.3V
RoHS compliant	Yes	Yes	Yes	Yes
Supports SOPC builder	Yes	Yes	Yes	Yes
Design Tool	Quartus	Quartus	Quartus	Quartus
Oper. Temp (C)	-40 to +85	-40 to +85	-40 to +85	-40 to +85
Storage Temp (C)	-55 to +105	-55 to +105	-55 to +105	-55 to +105
Conformal Coating	Optional	Optional	Optional	Optional
Cooling Method	Forced Air	Forced Air	Forced Air	Conduction
Private SDRAM	None	None	16 Mbytes	None
Parallel Flash	None	None	16 Mbytes	None
Application EEPROM	None	93LC66 64Kbit	93LC66 64Kbit	93LC66 64Kbit
Temperature Sensor	No	Yes	Yes	Yes
Nios Capable	Limited	Limited	Yes	Limited
uCLinux	No	No	Yes	No
PCI Bus Interface	PLX 9030	PLX 9056	PLX 9056	PLX 9056
PCI Signaling	Universal 3.3 or 5V	Universal 3.3 or 5V	Universal 3.3 or 5V	Universal 3.3 or 5V
PCI Bus Support	32b, 33 MHz	32b, up to 66 MHz	32b, up to 66 MHz	32b, up to 66 MHz
Front Panel I/O	68 pin SCSI style	68 pin SCSI style	68 pin SCSI style	None
Rear I/O	64 pin PN4	None	None	64 pin PN4
Breakout	Yes (eg, P/N 4988)	Yes (eg, P/N 4988)	Yes (eg, P/N 4988)	Yes (eg, P/N 4988)
PCI Master Capable	No	Yes	Yes	Yes
Download FPGA via PCI	Yes	Yes	Yes	Yes
FPGA Configuration EEPROM	Yes	Yes	Yes	Yes
Supports JTAG Debug	Yes	Yes	Yes	Yes