Comparison Table Technobox P/Ns 6070 and 2372

Feature	P/N 6070	P/N 2372
FPGA	EP1C4F324I7N (standard)	EPF10K70RC240-4N
Logic Element Count	4,000 (EP1C4 part)	3,744
FPGA (optional configuration)	EP1C12 12,060 LE EP1C20 20,060 LE	None larger
FPGA Ram	78,336 bits	18,432 bits
JTAG FPGA programming	Yes	No
Altera Signal tap debug	Yes via JTAG	No
FPGA Download over PCI	Yes *1	Yes
Download Device	In-circuit Reprogrammable, 4Mb, M25P40	One Time Programmable, 1Mb, EP1C1
FPGA Development tool	Quartus	Quartus or MaxPlus II
FPGA Migration tool	Yes *6	N/A
Digital I/O buffers	74ALV1642545 *2	74FCT16245
I/O interface voltage	+5V (Default) or +3.3V Resistor selectable	+5V only
I/O Front Panel termination	RC network *1	RC network
PN4 Rear I/O termination	None *1	None
SRAM	256K x 16	128K x 16
PCI to local bridge	PLX9030 *4	PLX9052
PCI signaling	+3.3V and +5V	+5V only
PMC voltage required	+5V only	+5V only
Supports ICS1522	Yes *5	Yes
Supports MAX902	Yes	Yes
Internal PLLs	2	none
Layer Count	8	6
SMT components	Double sided *7	Single sided
Price	Lower	Higher
RoHS Compliant	Yes	No
Local Bus width	16 bits *1	16 bits
Industrial Temperature	Yes *3	No

* NOTES

1. No change for compatibility
2. Allows for 3.3 volt or 5 volt I/O
3. Except for ICS1522 and MAX902; however, most applications do not use those compaonents
4. 9030 supports both 3.3 volt and 5 volt PCI signaling
5. Since the ICS1522 is obsolete it may be removed eventually
6. Allows migration of a design from P/N 2372 to P/N 6070
7. Allows for improved bypassing

Technobox®, inc.

Technobox, Inc.
140 Mount Holly Bypass
Unit 1
Lumberton, NJ 08048-1114
Tel: 609-267-8988 • Fax: 609-261-1011
www.technobox.com